

Appl. No. : 09/051,263
Filed : August 7, 1998

The specific changes to the substitute specification are shown on a separate set of pages attached hereto and entitled **VERSION WITH MARKINGS TO SHOW CHANGES MADE**, which follows the signature page of this Amendment. On this set of pages, the insertions are underlined while the **[deletions are in brackets and bolded]**.

The Examiner indicated that there are numerous blank pages in the specification. The blank pages have been removed in the substitute specification. Specifically, pages 22, 42, 58, 62 and 180 of the original specification have been removed. Applicant submits that these blank pages were inadvertently submitted with the application and that there is no indication of missing or removed subject matter as one technically skilled in the art can determine from the continuity of the text before and after the blank pages. The pages of the substitute specification have been renumbered to account for the removal of these blank pages. Applicant submits that no new matter has been added by this amendment.

The Examiner indicated that line 23 of page 29 of the originally filed specification indicates that Tables 34 and 37 appear below in the specification and that these tables list the mnemonics and opcodes. The Examiner pointed out that Table 34 actually appeared on page 65 and did not list the mnemonics and opcodes as stated. The substitute specification amends line 22 of page 29 of the originally filed specification to reference Tables 36 and 37, instead of Tables 34 and 37. Applicant points out that Table 37 actually appears on page 78 of the originally filed application and correctly lists the mnemonics and opcodes as stated. As the specification recites, the tables list the mnemonics and opcode. See Original Specification, page 29, line 23. Table 36 appears on page 77 of the originally filed specification and contains the title "MPU Mnemonics and Opcode (Mnemonic Order)." Thus, one technically skilled in the art would understand that the reference to Table 34 on line 22 of page 29 of the original specification was a typographical error and that the intent would have been to refer to Table 36, which as its title indicates, contains the information described on page 29, lines 22-23 of the original specification when referencing the table. The substitute specification further moves Tables 36 and 37 from pages 77 and 78, where they appeared in the original specification, to the pages directly following page 29, thereby positioning the tables more closely to the portion of the specification that references the tables. Applicant submits that the mere movement of the physical location of the tables in the specification and the correction of the typographical error adds no new matter to the application.

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The Examiner noted that the text appearing before and after Tables 52-54 on pages 155-157 of the original specification is unrelated to these tables. Applicant has moved Tables 52-54 from pages 155-157, where they appeared in the original specification, to the pages directly following page 142, thereby positioning the tables more closely to the portion of the specification that references those tables. Applicant submits that this amendment merely moves the physical location of the tables in the specification and that no new matter has been added.

The Examiner noted that page 181 makes reference to an appendix, but that no appendix was found in the specification. Applicant has deleted this reference to an appendix and a second reference on page 76 of the original specification to an appendix in the substitute specification. Applicant submits that the specification is complete and that the erroneous reference to appendices is not an indication of missing subject matter or an incomplete application. Accordingly, Applicant submits that no new matter has been added by these amendments.

Status of the Claims

Claims 1-44 are pending in the application and are presented for examination.

CONCLUSION

The Applicant has amended the Specification to overcome all of the comments detailed in the Official Communication dated May 15, 2000. The Applicant has endeavored to address all of the concerns as expressed in the Official Communication. Accordingly, amendments to the specification and the reasons therefor, are presented above. If the Examiner finds any remaining impediment to the prompt allowance of these claims that could be clarified with a telephone conference, the Examiner is respectfully requested to initiate the same with the undersigned.

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Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated:

July 12, 2002

By:

Drew S. Hamilton

Drew S. Hamilton
Registration No. 29,801
Attorney of Record
620 Newport Center Drive
Sixteenth Floor
Newport Beach, CA 92660
(619) 235-8550

VERSION WITH MARKINGS TO SHOW CHANGES

The substitute specification contains the following amendments:

Blank pages, consisting of pages 22, 42, 58, 62 and 180 of the original specification, are removed.

Tables 36 and 37 are deleted from pages 77 and 78 of the originally filed specification and added following page 29 of the originally filed specification.

Tables 52, 53 and 54 are deleted from pages 155, 156 and 157 of the originally filed specification and added following page 141 of the originally filed specification.

The single line paragraph beginning on page 76, line 1, of the originally filed specification is deleted.

The single line paragraph beginning on page 181, line 1, of the originally filed specification is deleted.

The paragraph beginning on page 29, line 22 of the original specification, has been replaced with the following paragraph:

Table 4 lists the MPU instructions; Table [34] 36, below, and Table 37, below, list the mnemonics and opcodes. All instructions consist of eight bits, except for those that require immediate data. This allows up to four instructions (an Instruction group) to be obtained on each instruction fetch, thus reducing memory-bandwidth requirements compared to typical RISC machines with 32-bit instructions. This characteristic also allows looping on an instruction group (a micro-loop) without additional instruction fetches from memory, further increasing efficiency. Instruction formats are depicted in Figure 6.